Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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DATA SHEET

PHOTOCOUPLER PS9817A-1,-2

HIGH CMR, 10 Mbps OPEN COLLECTOR OUTPUT TYPE 8-PIN SSOP (SO-8) HIGH-SPEED PHOTOCOUPLER

-NEPOC Series-

DESCRIPTION

The PS9817A-1 and PS9817A-2 are active-low type high-speed photocouplers that use a GaAlAs light-emitting diode on the input side and a photodetector IC that includes a photodiode and a signal processor on the same chip on the output side.

The PS9817A-1 and PS9817A-2 are designed specifically for high common mode transient immunity (CMR) and low pulse width distortion. The PS9817A-2 is suitable for high density applications.

FEATURES

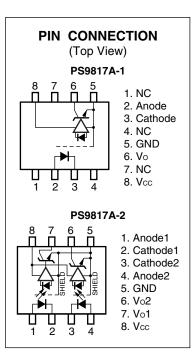
- Pulse width distortion ($|t_{PHL}-t_{PLH}| = 35 \text{ ns MAX.}$)
- High common mode transient immunity (CMH, CML = $\pm 15 \text{ kV}/\mu \text{s}$ MIN.)
- 40% reduction of mounting area (5-pin SOP × 2)
- High-speed (10 Mbps)
- High isolation voltage (BV = 2 500 Vr.m.s.)
- Open collector output
- Ordering number of tape product : PS9817A-1-F3: 1 500 pcs/reel

: PS9817A-2-F3: 1 500 pcs/reel

- Pb-Free product
- Safety standards
 - UL approved: File No. E72422
 - DIN EN60747-5-2 (VDE0884 Part2) approved: No. 40008347 (Option)

APPLICATIONS

- Measurement equipment
- PDP
- FA Network



TRUTH TABLE

LED	Output
ON	L
OFF	Н

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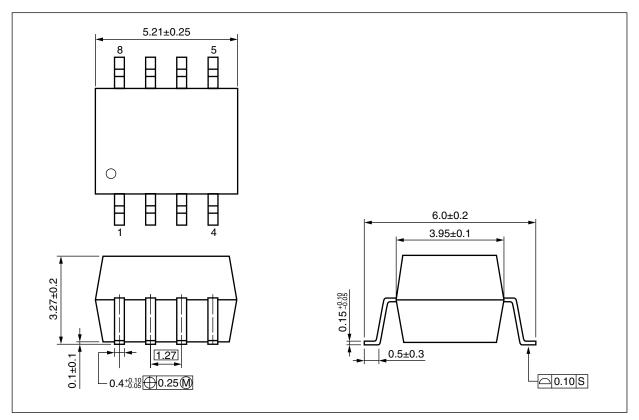
Document No. PN10647EJ03V0DS (3rd edition) Date Published August 2008 NS Printed in Japan

The mark <R> shows major revised points.

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The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

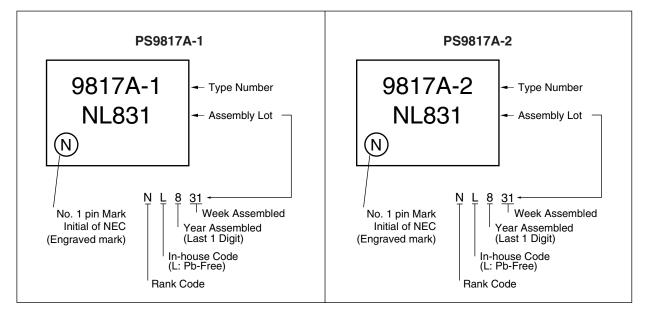
PACKAGE DIMENSIONS (UNIT: mm)



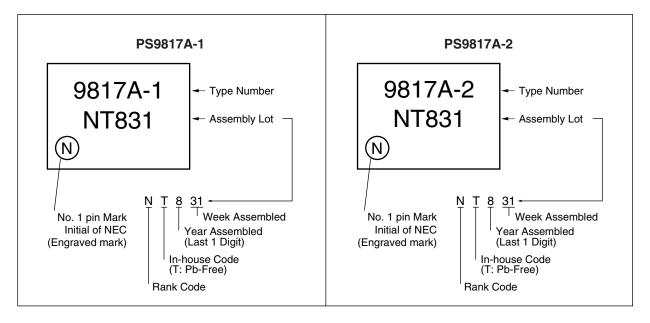
NEC

<R> MARKING EXAMPLE

SnBi PLATING



Ni/Pd/Au PLATING



<R> ORDERING INFORMATION

Part Number	Order Number	Solder plating specification	Packing Style	Safety Standards Approval	Application Part Number [⊶]
PS9817A-1	PS9817A-1-A	Pb-Free	20 pcs (Tape 20 pcs cut)	Standard products	PS9817A-1
PS9817A-1-F3	PS9817A-1-F3-A	(SnBi)	Embossed Tape 1 500 pcs/reel	(UL approved)	
PS9817A-2	PS9817A-2-A		20 pcs (Tape 20 pcs cut)		PS9817A-2
PS9817A-2-F3	PS9817A-2-F3-A		Embossed Tape 1 500 pcs/reel		
PS9817A-1-V	PS9817A-1-V-A		20 pcs (Tape 20 pcs cut)	DIN EN60747-5-2	PS9817A-1
PS9817A-1-V-F3	PS9817A-1-V-F3-A		Embossed Tape 1 500 pcs/reel	(VDE0884 Part2)	
PS9817A-2-V	PS9817A-2-V-A		20 pcs (Tape 20 pcs cut)	approved	PS9817A-2
PS9817A-2-V-F3	PS9817A-2-V-F3-A		Embossed Tape 1 500 pcs/reel	(Option)	
PS9817A-1	PS9817A-1-AX	Pb-Free	20 pcs (Tape 20 pcs cut)	Standard products	PS9817A-1
PS9817A-1-F3	PS9817A-1-F3-AX	(Ni/Pd/Au)	Embossed Tape 1 500 pcs/reel	(UL approved)	
PS9817A-2	PS9817A-2-AX		20 pcs (Tape 20 pcs cut)		PS9817A-2
PS9817A-2-F3	PS9817A-2-F3-AX		Embossed Tape 1 500 pcs/reel		
PS9817A-1-V	PS9817A-1-V-AX		20 pcs (Tape 20 pcs cut)	DIN EN60747-5-2	PS9817A-1
PS9817A-1-V-F3	PS9817A-1-V-F3-AX		Embossed Tape 1 500 pcs/reel	(VDE0884 Part2)	
PS9817A-2-V	PS9817A-2-V-AX		20 pcs (Tape 20 pcs cut)	approved	PS9817A-2
PS9817A-2-V-F3	PS9817A-2-V-F3-AX		Embossed Tape 1 500 pcs/reel	(Option)	

*1 For the application of the Safety Standard, following part number should be used.

ABSOLUTE MAXIMUM RATINGS (TA = 25°C, unless otherwise specified)

Parameter		Symbol	Ratings		Unit		
			PS9817A-1	PS9817A-2			
Diode	Forward Current	lF	20*1	15 ^{*2}	mA/ch		
	Reverse Voltage	VR	5		V/ch		
Detector	Supply Voltage	Vcc	7		V		
	Output Voltage	Vo	7		V/ch		
	Output Current	lo	25		mA/ch		
	Power Dissipation "3	Pc	40		mW/ch		
Isolation	Voltage *4	BV	2 500		2 500		Vr.m.s.
Operating	g Ambient Temperature	TA	-40 to +85		°C		
Storage -	Temperature	Tstg	-55 to +125		°C		

*1 Reduced to 0.3 mA/°C at $T_A = 60°C$ or more.

*2 Reduced to 0.1 mA/°C at $T_A = 60°C$ or more.

- *3 Applies to output pin Vo (collector pin). Reduced to 1.5 mW/°C at TA = 65° C or more.
- *4 AC voltage for 1 minute at $T_A = 25^{\circ}$ C, RH = 60% between input and output. Pins 1-4 shorted together, 5-8 shorted together.

RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Low Level Input Voltage	VFL	0		0.8	V
High Level Input Current	Ігн	6.3	10	12.5	mA
Supply Voltage	Vcc	4.5		5.5	V
Pull-up Resistance	R∟	330		4 k	Ω
TLL (R _L = 1.0 k Ω , loads)	Ν			5	

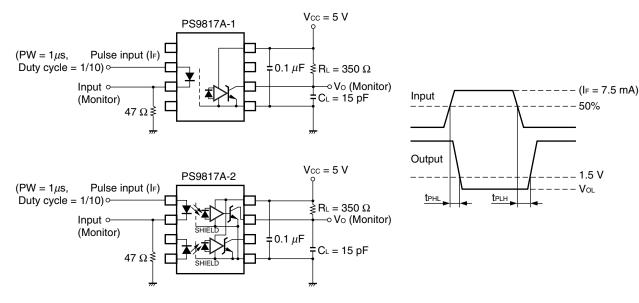
ELECTRICAL CHARACTERISTICS (T_A = -40 to +85°C, unless otherwise specified)

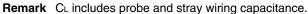
	Parameter	Symbol	Conditions	MIN.	TYP. ^{*¹}	MAX.	Unit
Diode	Forward Voltage	VF	IF = 10 mA, TA = 25°C	1.4	1.65	1.8	V
	Reverse Current	IR	V _R = 3.0 V, T _A = 25°C			10	μA
	Terminal Capacitance	Ct	VF = 0 V, f = 1 MHz, TA = 25°C		30		pF
Detector	High Level Output Current	Іон	$V_{CC} = V_{O} = 5.5 \text{ V}, \text{ V}_{F} = 0.8 \text{ V}$		1	100	μA
	Low Level Output Voltage ²	Vol	Vcc = 5.5 V, I⊧ = 5.0 mA, Io∟ = 13 mA		0.2	0.6	V
	High Level Supply Current (PS9817A-1)	Іссн	$V_{CC} = 5.5 \text{ V}, \text{ I}_F = 0 \text{ mA}, \text{ Vo} = \text{open}$		4	7	mA
	High Level Supply Current (PS9817A-2)				8	14	
	Low Level Supply Current (PS9817A-1)	lcc∟	$V_{CC} = 5.5 \text{ V}, \text{ I}_{F} = 10 \text{ mA}, \text{ V}_{O} = \text{open}$		6	10	
	Low Level Supply Current (PS9817A-2)				12	20	
Coupled	Threshold Input Current $(H \rightarrow L)$	Ifhl	$V_{CC} = 5.0 \text{ V}, \text{ Vo} = 0.8 \text{ V}, \text{ R}_{\text{L}} = 350 \ \Omega$		2	5	mA
	Isolation Resistance	Ri-o	$V_{I-O} = 1 \text{ kV}_{DC}$, $RH = 40 \text{ to } 60\%$, $T_A = 25^{\circ}C$	10 ¹¹			Ω
	Insulation Resistance (Input-Input), (PS9817A-2)	R⊦ı	$V_{I-1} = 1 \text{ kV}_{DC}, \text{ RH} = 40 \text{ to } 60\%,$ $T_A = 25^{\circ}\text{C}$	10 ¹⁰			Ω
	Isolation Capacitance	CI-O	V = 0 V, f = 1 MHz, T _A = 25°C		0.6		pF
	Insulation Capacitance (Input-Input), (PS9817A-2)	CI-I	V = 0 V, f = 1 MHz, T _A = 25°C		0.3		pF
	Propagation Delay Time	t PHL	T _A = 25°C		40	75	ns
	$(H \rightarrow L)^{*3}$					100	
	Propagation Delay Time	t PLH	T _A = 25°C		45	75	
	$(L \rightarrow H)^{3}$					100	
	Rise Time	tr	Vcc = 5.0 V, R∟ = 350 Ω, I⊧ = 7.5 mA		20		
	Fall Time	tr			5		
	Pulse Width Distortion (PWD) ^{*3}	tphl-tplh			5	35	
	Propagation Delay Skew	t PSK				40]
	Common Mode Transient Immunity at High Level Output [™]	СМн		15	20		kV/ <i>µ</i> s
	Common Mode Transient Immunity at Low Level Output ³⁴	CM∟	$ V_{CC} = 5.0 \text{ V}, \text{ R}_{\text{L}} = 350 \ \Omega, \text{ T}_{\text{A}} = 25^{\circ}\text{C}, \\ I_{\text{F}} = 7.5 \text{ mA}, \text{ V}_{\text{O}} < 0.8 \text{ V}, \text{ V}_{\text{CM}} = 1 \text{ kV} $	15	20		

*1 Typical values at $T_A = 25^{\circ}C$

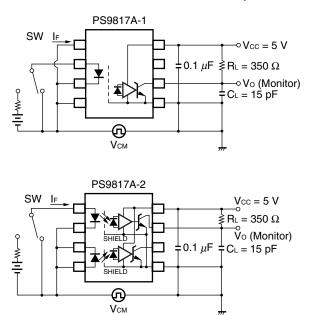
*2 Because VoL of 2 V or more may be output when LED current input and when output supply of Vcc = 2.6 V or less, it is important to confirm the characteristics (operation with the power supply on and off) during design, before using this device.

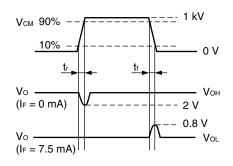
*3 Test circuit for propagation delay time





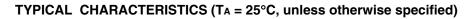
*4 Test circuit for common mode transient immunity

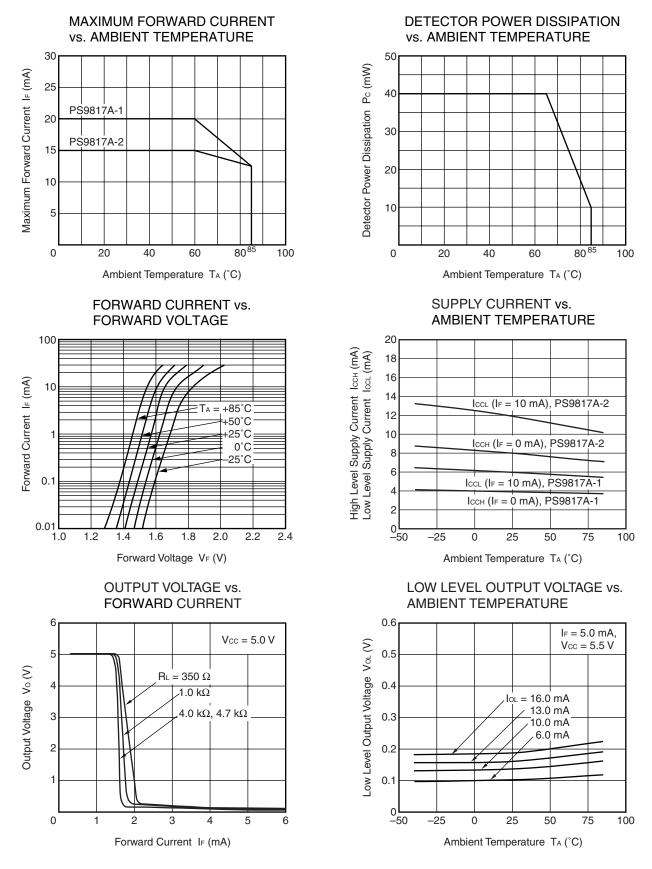




USAGE CAUTIONS

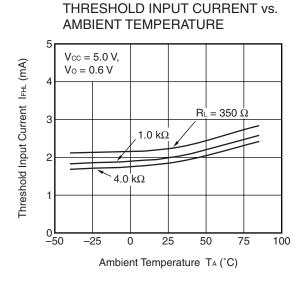
- 1. This product is weak for static electricity by designed with high-speed integrated circuit so protect against static electricity when handling.
- **2.** By-pass capacitor of 0.1 μ F is used between Vcc and GND near device. Also, ensure that the distance between the leads of the photocoupler and capacitor is no more than 10 mm.
- 3. Avoid storage at a high temperature and high humidity.



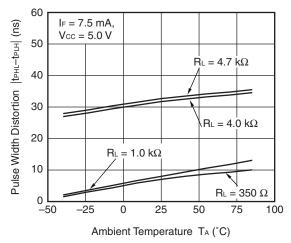




Data Sheet PN10647EJ03V0DS

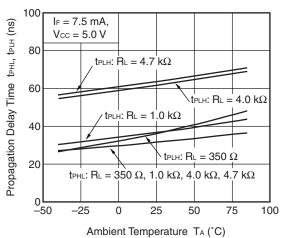


PULSE WIDTH DISTORTION vs. AMBIENT TEMPERATURE

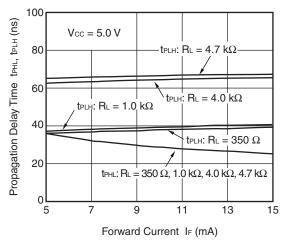


Remark The graphs indicate nominal characteristics.

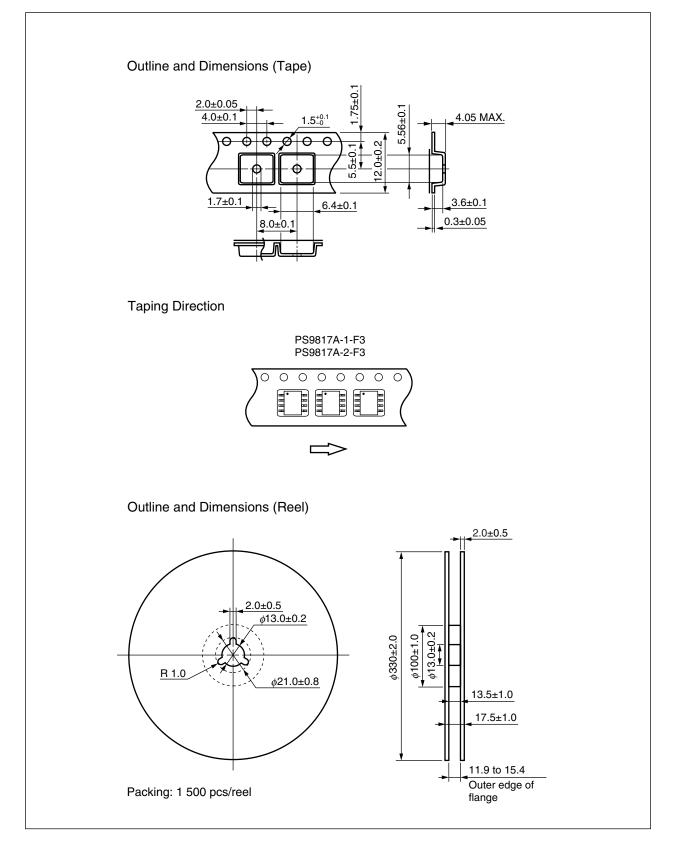
PROPAGATION DELAY TIME vs. AMBIENT TEMPERATURE



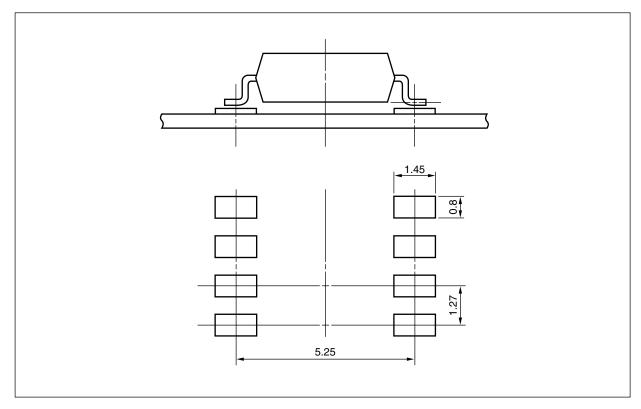
PROPAGATION DELAY TIME vs. FORWARD CURRENT



TAPING SPECIFICATIONS (UNIT: mm)



RECOMMENDED MOUNT PAD DIMENSIONS (UNIT: mm)



NOTES ON HANDLING

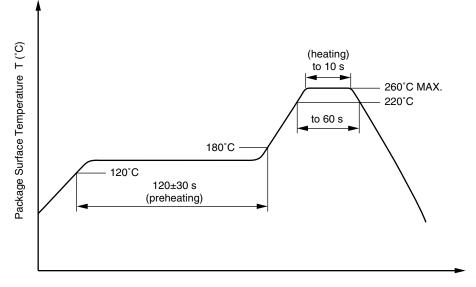
1. Recommended soldering conditions

(1) Infrared reflow soldering

- Peak reflow temperature
- Time of peak reflow temperature
- Time of temperature higher than 220°C
- Time to preheat temperature from 120 to 180°C
- Number of reflows
- Flux

260°C or below (package surface temperature) 10 seconds or less 60 seconds or less 120±30 s Three Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

Recommended Temperature Profile of Infrared Reflow



Time (s)

(2) Wave soldering

- Temperature 260°C or below (molten solder temperature)
- Time 10 seconds or less
- Preheating conditions 120°C or below (package surface temperature)
- Number of times One (Allowed to be dipped in solder including plastic mold portion.)
- Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

(3) Soldering by Soldering Iron

Peak Temperature (lead part temperature)	350°C or below
 Time (each pins) 	3 seconds or less
• Flux	Rosin flux containing small amount of chlorine (The flux with a
	maximum chlorine content of 0.2 Wt% is recommended.)

(a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead

(b) Please be sure that the temperature of the package would not be heated over 100°C

(4) Cautions

• Fluxes

Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.

2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output or between collector-emitters at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

USAGE CAUTIONS

- 1. Protect against static electricity when handling.
- 2. Avoid storage at a high temperature and high humidity.

<R> SPECIFICATION OF VDE MARKS LICENSE DOCUMENT

Parameter	Symbol	Speck	Unit
Application classification (DIN EN 60664-1 VDE0110 Part 1) for rated line voltages \leq 300 Vr.m.s. for rated line voltages \leq 600 Vr.m.s.		IV III	
Climatic test class (DIN EN 60664-1 VDE0110)		40/85/21	
Dielectric strength maximum operating isolation voltage Test voltage (partial discharge test, procedure a for type test and random test) $U_{pr} = 1.5 \times U_{IORM}, P_d < 5 pC$	Uiorm Upr	566 849	V _{peak} V _{peak}
Test voltage (partial discharge test, procedure b for all devices) U_{pr} = 1.875 \times U_{IORM}, P_{d} < 5 pC	Upr	1 061	V _{peak}
Highest permissible overvoltage	Utr	4 000	Vpeak
Degree of pollution (DIN EN 60664-1 VDE0110 Part 1)		2	
Clearance distance		>4.0	mm
Creepage distance		>4.0	mm
Comparative tracking index (DIN IEC 112/VDE 0303 Part 1)	СТІ	175	
Material group (DIN EN 60664-1 VDE0110 Part 1)		III a	
Storage temperature range	Tstg	-55 to +125	°C
Operating temperature range	TA	-40 to +85	°C
Isolation resistance, minimum value $V_{IO} = 500 \text{ V dc at } T_A = 25^{\circ}\text{C}$ $V_{IO} = 500 \text{ V dc at } T_A MAX. at least 100^{\circ}\text{C}$	Ris MIN. Ris MIN.	10 ¹² 10 ¹¹	Ω Ω
Safety maximum ratings (maximum permissible in case of fault, see thermal derating curve) Package temperature Current (input current IF, Psi = 0) Power (output or total power dissipation)	Tsi Isi Psi	150 150 600	°C mA mW
Isolation resistance V _{IO} = 500 V dc at T _A = Tsi	Ris MIN.	10 [°]	Ω

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M8E 02.11-1

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	• Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
	 Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
	Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
	• Do not burn, destroy, cut, crush, or chemically dissolve the product.
	• Do not lick the product or in any way allow it to enter the mouth.